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APPLICANTS

Joachim Brilka, Hamburg, GERMANY;
 Axel Kattner, Seevetal, GERMANY;
 Ernst-Peter Ragosche, Stade, GERMANY;

*verified
rcj*

** CONTINUING DATA *****

This application is a 371 of PCT/IB04/00752 03/16/2004

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 03100720.6 03/20/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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| Foreign Priority claimed | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | STATE OR COUNTRY | SHEETS DRAWING | TOTAL CLAIMS | INDEPENDENT CLAIMS |
|---------------------------------|--|------------------|----------------|--------------|--------------------|
| 35 USC 119 (a-d) conditions met | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | GERMANY | 2 | 6 | 2 |

ADDRESS

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TITLE

Circuit arrangement and transistor control method

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